

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of Docket No: Q78002
Yoshihiro NONAKA Group Art Unit: 2816
Appln. No.: 10/684,441 Examiner: Anh Quan TRA
Confirmation No.: 8870 Patent No.: 7,005,912
Filed: October 15, 2003 Issue Date: February 28, 2006
For: SIMPLE STEP-UP APPARATUS INCLUDING LEVEL SHIFT CIRCUITS CAPABLE OF LOW BREAKDOWN VOLTAGE

REQUEST FOR CERTIFICATE OF CORRECTION

ATTN: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

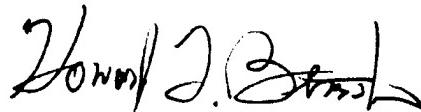
Sir:

Pursuant to the provisions of 37 C.F.R. § 1.323, please enter the attached Certificate of Correction.

Since the errors noted are believed to be the Applicant's responsibility, the statutory fee of \$100.00 is being charged to Deposit Account No. 19-4880 via EFS Payment Screen. The USPTO is directed and authorized to charge all required fees to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

In view of the foregoing, issuance of the Certificate of Correction is respectfully requested.

Respectfully submitted,



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WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: August 21, 2007

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO: 7,005,912
DATED: February 28, 2006
INVENTOR(S): Yoshihiro NONAKA

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, line 58, delete "32", insert --33--;
Column 4, line 59, delete both occurrences of "N₂", insert --N₃--;
Column 4, line 60, delete "32", insert --33--;
Column 4, line 62, delete "N₂", insert --N_k--;
Column 4, line 63, delete "N₂", insert --N_k--;
Column 5, line 27, delete "32", insert --42--;
Column 5, line 29, delete "N₂", insert --N_k--;
Column 5, line 30, delete "N₂", insert --N_k--;
Column 11, line 47, delete "third", insert --first--;
Column 13, line 21, delete "third", insert --first--;
Column 14, line 34, delete "as", insert --at--;
Column 14, line 36, delete "third", insert --first--;
Column 16, line 23, delete "third", insert --first--;
Column 15, line 52, after "voltages", insert --said first positive voltage being smaller than said second positive voltage--;
Column 17, line 42, delete "third", insert --first--;
Column 19, line 25, delete "third", insert --first--;
Column 20, line 65, after "and", insert --second--;
Column 22, line 50, after "and", insert --second--;
Fig. 11, drawing sheet 10 of 30, delete "φ23", insert --φ2--, delete "5A", insert ---7A--;
Fig. 21, drawing sheet 20 of 30, delete "3R", insert --3A--.

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PATENT NO. 7,005,912

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